Silicon N-Channel MOS FET

HITACHI

November 1996

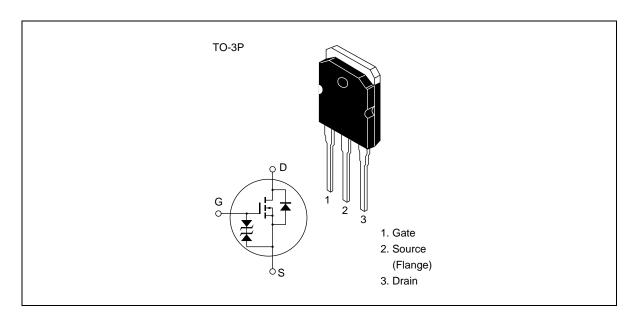
Application

Low frequency power amplifier Complementary pair with 2SJ351, 2SJ352

Features

- High power gain
- Excellent frequency response
- High speed switching
- Wide area of safe operation
- Enhancement-mode
- Good complementary characteristics
- Equipped with gate protection diodes

Outline



Ordering Information

Type No.	VDSS
2SK2220	180 V
2SK2221	200 V

Absolute Maximum Ratings $(Ta = 25^{\circ}C)$

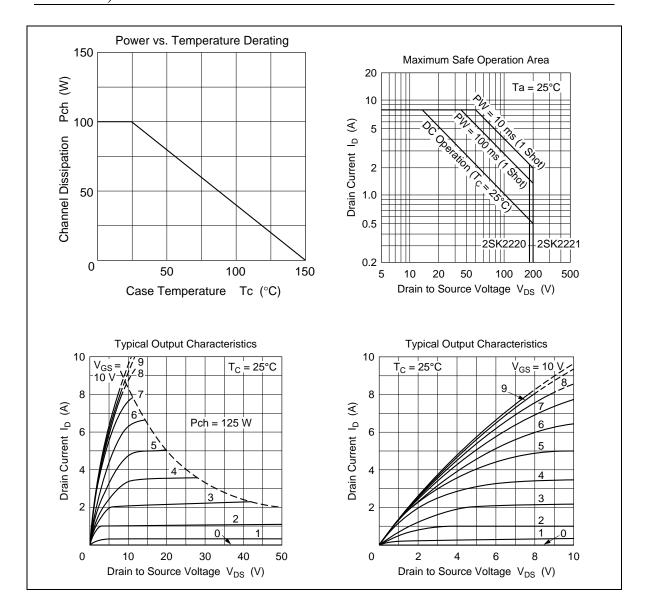
	Symbol	Ratings	Unit	
2SK2220	V _{DSX}	180	V	
2SK2221		200		
	V _{GSS}	±20	V	
	I _D	8	А	
Body to drain diode reverse drain current		8	А	
	Pch*1	100	W	
	Tch	150	°C	
	Tstg	-55 to +150	°C	
	2SK2221	$ \begin{array}{c c} 2\text{SK2220} & V_{\text{DSX}} \\ \hline 2\text{SK2221} & V_{\text{GSS}} \\ & I_{\text{D}} \\ \text{n current} & I_{\text{DR}} \\ & \text{Pch*}^1 \\ \hline & \text{Tch} \\ \end{array} $	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	

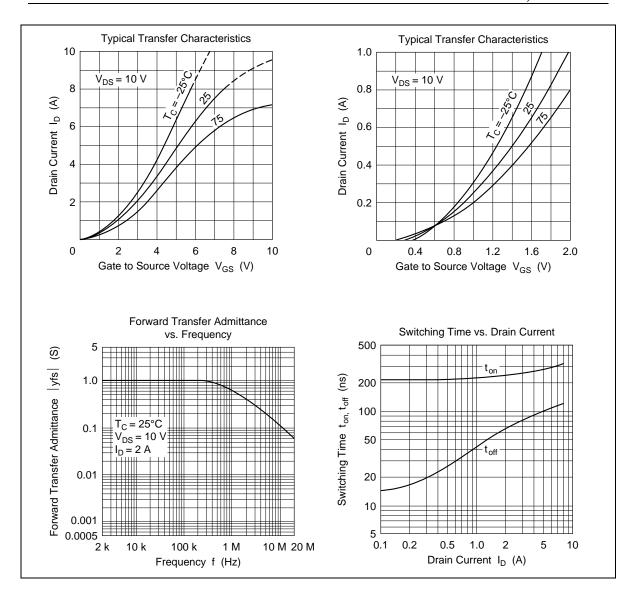
Note 1. Value at Tc = 25 °C

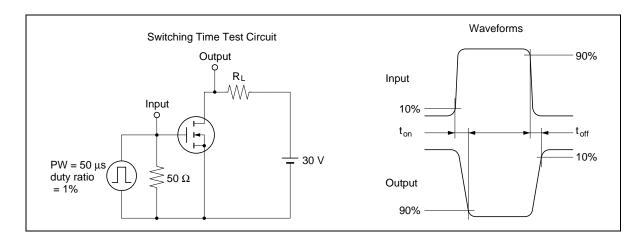
Electrical Characteristics (Ta = 25°C)

Item		Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	2SK2220	$V_{(BR)DSX}$	180		_	V	$I_{D} = 10 \text{ mA}, V_{GS} = -10 \text{ V}$
	2SK2221	_	200	_	_	_	
Gate to source by voltage	reakdown	$V_{(BR)GSS}$	±20	_	_	V	$I_{_{G}} = \pm 100 \ \mu A, \ V_{_{DS}} = 0$
Gate to source cu	utoff voltage	$V_{GS(off)}$	0.15	_	1.45	V	$I_{D} = 100 \text{ mA}$ $V_{DS} = 10 \text{ V}$
Drain to source s voltage	aturation	$V_{\scriptscriptstyle DS(sat)}$	_	_	12	V	$I_D = 8 \text{ A}, V_{GD} = 0 \text{ V*}^1$
Forward transfer	admittance	y _{fs}	0.7	1.0	1.4	S	$I_{D} = 3 \text{ A}$ $V_{DS} = 10 \text{ V}^{*1}$
Input capacitance	;	Ciss	_	600	_	pF	$V_{GS} = -5 \text{ V}$ $V_{DS} = 10 \text{ V}$ f = 1 MHz
Output capacitan	се	Coss	_	800	_	pF	
Reverse transfer	capacitance	Crss		8		pF	
Turn-on time		t _{on}	_	250	_	ns	V _{DD} = 30 V
Turn-off time		t _{off}	_	90	_	ns	I _D = 4 A

Note 1. Pulse Test







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